

**PATENT NUMBER and
ISSUE DATE**

U.S. UTILITY Patent Application

APPL. NUM	FILING DATE	CLASS	SUBCLASS	CAU	EXAMINER
10021406	10/30/2001	365		2618	
APPLICANTS: Enabce Michael; Hamlin Christopher 7462					
SEARCHED AND SEARCHING DATA VERIFIED:					
BEST AVAILABLE COPY					
SEARCHED AND SEARCHING DATA VERIFIED:					
PUBLISHED OR NOT PUBLISHED <input checked="" type="checkbox"/> RESCIND <input type="checkbox"/>					
Serial priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no					
5 USC 113 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no					
verified and acknowledged Examiner's initials					
T1111: System and method for optimizing an integrated circuit design					
101-80					
NO PCT OR PCT/US CLAIMS					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
<input checked="" type="checkbox"/> TERMINAL		Primary Examiner	Application Examiner	
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